
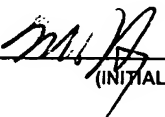


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Original Patent Number of Patent To Be Reissued is 5,903,260		The Maintenance fee status is: <input checked="" type="checkbox"/> up to date. <input type="checkbox"/> not required.
This reissue patent is subject to A Terminal Disclaimer that: <input type="checkbox"/> was filed during the prosecution of the reissue application. <input type="checkbox"/> was of record prior to the filing of the reissue application.		
Physical surrender of the letters patent <input type="checkbox"/> was made. <input type="checkbox"/> was not made, but a statement of loss/inaccessibility was provided. <input type="checkbox"/> is not required		

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[4. The method according to claim 3, wherein the flat display panel module unit is arranged separately from the display control unit.]

[5. A flat display unit comprising:

a flat display panel for being driven in accordance with display driving voltages;

display driver means for selecting the display driving voltages supplied to said flat display panel, said display driver means comprising a logic circuit and a detection means for detecting a logic power voltage/activating said logic circuit and for supplying a power control signal having a delay time after the detection of the logic power voltage; and

a display power source circuit for supplying the display driving voltages to said display driver means in response to the power control signal.]

[6. A flat display device comprising a flat display panel module unit and a display control unit for supplying control signals to control display of the flat display panel module unit.

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said flat display panel module unit comprising:

a flat display panel driven in accordance with display driving voltages;

display driver means for selecting the display driving voltages supplied to said flat display panel and for supplying a power control signal having a delay time after a logic power voltage has been supplied to a logic circuit of said display driver means; and

a display power source circuit for supplying the display driving voltages to said display driver means in response to the power control signal.

wherein said display driver means starts the selection of the display driving voltages in response to a display start signal having a delay time after the power control signal has supplied to said power source circuit.]

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'a'
comma
after
"voltage"
should
be deleted
as per
certificate
of correction
in original patent 5,903,260

M.A.
5/8/08